



||Jai Sri Gurudev||
Sri AdichunchanagiriShikshana Trust (R)

SJB INSTITUTE OF TECHNOLOGY

(Affiliated to Visvesvaraya Technological University, Belgavi & Approved by AICTE, New Delhi)

No. 67, BGS Health & Education City, Dr. Vishnuvardhan Road, Kengeri, Bengaluru-560060.

Web: www.sjbit.edu.in, Email: principal@sjbit.edu.in, Phone: 080-28612445/46, Fax: 080-28612651



DEPARTMENT OF ELECTRONICS &  COMMUNICATION ENGINEERING



























Ref : SJBIT/ECE/2020-21/878

Date : 16 /07/2021

CIRCULAR

The subject list for **Odd Sem 2021-22** is enclosed. The faculty members are informed to give their subject preferences for **Odd Sem 2021-22**, in the enclosed sheet against their names on or before 20/07/2021. All senior faculty members are informed to give preference for UG subjects.


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	Name of the Faculty Member	Sign	Sl. No.	Name of the Faculty Member	Sign	Sl. No.	Name of the Faculty Member	Sign
1	Dr. Rekha K.R.		11	Dr. M Lakshminarayan		21	Jyothi H	
2	Dr. Chandrappa D N		12	Dr. Sunitha YN		22	Sowmya B J	
3	Dr. Vijayakumar T		13	Dr. D Kumutha		23	Anushree R	
4	Dr. K Somashekar		14	Chetana R		24	Supriya M	
5	Dr. Komala M		15	Bhaskar B		25	Geethanjali N	
6	Dr. Mahantesh K		16	Latha S		26	Rahul R Rai	
7	Dr. Ravikumar A V		17	Nithya S		27	Darshan BD	
8	Dr. Shilpa K Gowda	--	18	Pushpalatha G				
9	Dr. Supreeth H S G		19	Divyashree Y V				
10	Dr. Anitha P		20	Uma S.				



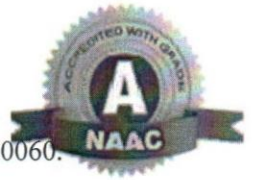
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III Sem

Sl. No.	Subject Code	Subject Title
1	18MAT31	Transform calculus, Fourier Series & Numerical Techniques
2	18EC32	Network Theory
3	18EC33	Electronic Devices
4	18EC34	Digital System Design
5	18EC35	Computer Organization & Architecture
6	18EC36	Power Electronics & Instrumentation
7	18ECL37	Electronic Devices & Instrumentation Laboratory
8	18ECL38	Digital System Design Laboratory
9	18KVK39/18KAV39	Kannada

V Sem

Sl. No.	Subject Code	Subject Title
1	18ES51	Technological Innovation Management And Entrepreneurship
2	18EC52	Digital Signal Processing
3	18EC53	Principles of Communication Systems
4	18EC54	Information Theory & Coding
5	18EC55	Electromagnetic Waves
6	18EC56	Verilog HDL
7	18ECL57	Digital Signal Processing Laboratory
8	18ECL58	HDL Laboratory
9	18CIV59	Environmental Studies

VII sem

Sl. No.	Subject Code	Subject Title
1	18EC71	Computer Networks
2	18EC72	VLSI Design
3	18XX73X	Professional Elective - 2
4	18EC74X	Professional Elective-3
5	18XX75X	Open elective - B
6	18ECL76	Computer Networks Lab
7	18ECL77	VLSI Lab
8	18ECP78	Project Work Phase - 1
9	-	Internship

Professional Elective-2

18EC731 - Real Time Systems ✓
18EC732 - Satellite Communication ✓
18EC733 - Digital Image Processing ✓
18EC734 - DSP Algorithms & Architecture ✓

Professional Elective-3

-18EC741 - IOT and Wireless Sensor Networks ✓
-18EC742 - Automotive Electronics ✓
18EC743 - Multimedia Communication ✓
-18EC744 - Cryptography ✓
-18EC745 - Machine Learning with Python ✓

Open Elective - B

18EC751 - Communication Theory
18EC752 - Neural Networks
18EC753 - ARM Embedded Systems
18EC754 - Digital Systems Design using VHDL

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1st M.Tech

Sl. No.	Subject Code	Subject Title
1	20ELD11	Advanced Engineering Mathematics
2	20EVE12	ASIC Design
3	20EVE13	Advanced Embedded system
4	20EVE14	VLSI Testing
5	20EVE15	Digital VLSI Design
6	20EVEL16	VLSI & ES LAB-1
7	20RM117	RESEARCH Methodology and IPR

Note: Subjected to change as per new Scheme & Syllabus (1st M.tech)

III Sem - PG

Sl. No.	Subject Code	Subject Title
1	20EVE31	CAD of Digital Systems
2	20XXX32X	Professional elective 3
3	20XXX33X	Professional elective 4
4	20EVE34	Project Work phase -1
5	20EVE35	Mini-Project
6	20EVE136	Internship



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Sl. No.	Name of the faculty	Sem	Sub Code	No. of times handled	Sem	Sub Code	No. of times handled	Sem	Sub Code	No. of times handled
1.	Dr. KV Mahendra Prashanth									
2.	Dr. Rekha K.R.	3	18EC36	8	PG 3	20EVE322	3	7	18EC742	08
3.	Dr. Chandrappa D N	PG I	20EVE13	3	3	18EC36	1	7	18EC733	02
4.	Dr. Vijayakumar T	3	18EC32	8	5	18EC56	-	3	18EC35	-
5.	Dr. K Somashekar	5	18EC54	6	iii	18EC36	1	3	18EC34	4
6.	Dr. Komala M	3	18EC35	5	7 th	18EC741	2	7	18EC731	4
7.	Dr. Mahantesh K	5 th	18EC55	5	7 th	18EC733	03	7	18EC743	4
8.	Dr. Ravikumar A V	3	18EC34	12	5	18EC54	-	3	18EC36	1
9.	Dr. Shilpa K Gowda	5	18EC54	05	5	18EC53	02	3	18EC34	01
10.	Dr. Supreeth H S G	7	18EC733	05	7	18EC745	02	5	18EC56	02
11.	Dr. Anitha P	3	18EC32	04	5	18EC53	03	7	18EC732	-
12.	Dr. M Lakshminarayan	3	18EC34/32	8	5	18EC52	6	7	18EC733/734	8
13.	Dr. Sunitha YN	3	18EC32	3	7	18EC732	2	5	18EC51	2
14.	Dr. D Kumutha	3	18EC34	4	3	18EC33	3	7	18EC71	5
15.	Chetana R	PG 3	20EVE31 18EC36	2 3	7	18EC72	4	7	18EC744	2
16.	Bhaskar B	3	18EC33	3	5	18EC53	9	7	18EC745	-
17.	Latha S	5	18EC52	5	5	18EC56	1	7	18EC734	8
18.	Nithya S	7	18EC731	3	mtech 1	20EVE12	3	5	18EC56	-
19.	Pushpalatha G	5	18EC56	-	7	18EC71 18EC745	3 -	1 st mtech	20EVE14	4
20.	Divyashree Y V	5	18EC56	2	5	18EC54	1	7	18EC71	1
21.	Uma S.	3	18EC35	2	5	18EC53	1	7	18EC744	-
22.	Jyothi H	3	18EC35	-	3	18EC36	-	5	18EC54	-
23.	Sowmya B J	5	18EC52	5	3	18EC35	1	5	18EC56	-
24.	Anushree R	3	18EC34	-	5	18EC55	1	7	18EC733	-

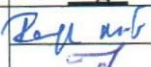
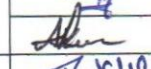
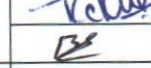
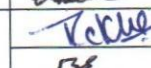


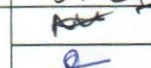
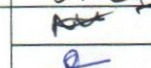
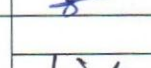
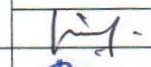
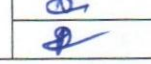
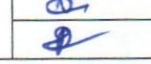




DEPARTMENT OF ELECTRONICS &  COMMUNICATION ENGINEERING

Date: 28/9/2021

Technical Staff Lab Allotment Details

Academic Year: Odd Sem (2021-22)

Sl. No.	Sem	Name of the Lab	Name of the staff	Sign
1	3 rd	EDI Lab	Raghu MT, Virupakasha Akilesh	  
2		DSD Lab	Rekharani S Santhosh Kumar B	 
3	5 th	DSP Lab	Vani K Rohith	 
4		HDL Lab	Paramesh MS Sahana V	 
5	7 th	VLSI Lab	Prasanna Kumar B Vijayalakshmi R	 
6		CN Lab	A Chandrashekar Shivaraju AN	 


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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SUBJECT ALLOTMENT (UG) – ODD SEMESTER 2021 – 2022

III Semester

Class Teacher:

III A: Dr. Komala M

III B: Mrs. Jyothi H

Sl. No	Subject Code	Subject Title	Name of the Faculty			
			Section A	Sign	Section B	Sign
Theory Subjects						
1	18MAT31	Mathematics				
2	18EC32	Network Theory	Dr. T Vijaykumar	<i>T</i>	Dr. T Vijaykumar	<i>T</i>
3	18EC33	Electronic Devices	Mr. Bhaskar B	<i>B</i>	Mr. Bhaskar B	<i>B</i>
4	18EC34	Digital System Design	Dr. Ravikumar A V	<i>av</i>	Dr. Ravikumar A V	<i>av</i>
5	18EC35	Computer Organization & Architecture	Dr. Komala M	<i>K</i>	Mrs. Jyothi H	<i>J</i>
6	18EC36	Power Electronics & Instrumentation	Dr. K R Rekha	<i>K</i>	Dr. K R Rekha	<i>K</i>
Laboratory						
7	18ECL37	Electronic Devices & Instrumentation Laboratory	Dr. K Somashekar Dr. Anitha P Mrs. Anushree R	<i>K</i> <i>A</i> <i>A</i>	Dr. Sunitha Y N Mr. Bhaskar B (B1 & B2) Dr. K R Rekha (B3), Mr. Rahul (B1), Mrs. Latha S (B2)	<i>S</i> <i>B</i> <i>R</i>
8	18ECL38	Digital System Design Laboratory	Dr. Ravikumar A V Mr. Darshan B D	<i>av</i> <i>D</i>	Dr. Komala M Dr. Shilpa K Gowda	
HSMC: Humanity and Social Science						
9	18CPC39	Constitution of India, Professional Ethics and Cyber Law (1 hour/week)				

[Signature]
Co-ordinator
3/8/21

[Signature]
3.8.2021
HOD
Head

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SUBJECT ALLOTMENT (UG) – ODD SEMESTER 2021 – 2022

V Semester

Class Teacher: V A: Mrs. Latha S

V B: Mrs. Divyashree Y V

Sl. No	Subject Code	Subject Title	Name of the Faculty			
			Section A	Sign	Section B	Sign
Theory Subjects						
1	18ES51	Technological Innovation Management And Entrepreneurship	Dr. Sunitha Y N	<i>[Signature]</i>	Dr. Sunitha Y N	<i>[Signature]</i>
2	18EC52	Digital Signal Processing	Mr. Rahul Rai	<i>[Signature]</i>	Dr. M Lakshminarayana	<i>[Signature]</i>
3	18EC53	Principles of Communication Systems	Dr. Shilpa K Gowda		Dr. Shilpa K Gowda	
4	18EC54	Information Theory & Coding	Dr. Somashekar K	<i>[Signature]</i>	Mrs. Divyashree YV	<i>[Signature]</i>
5	18EC55	Electromagnetic Waves	Dr. Mahantesh K	<i>[Signature]</i>	Dr. Mahantesh K	<i>[Signature]</i>
6	18EC56	Verilog HDL	Mrs. Latha S	<i>[Signature]</i>	Dr. Supreeth H S G	<i>[Signature]</i>
Laboratory						
7	18ECL57	Digital Signal Processing Laboratory	Mr. Rahul Rai Mrs. Sowmya B J	<i>[Signature]</i>	Dr. M Lakshminarayana Dr. Mahantesh K	<i>[Signature]</i>
8	18ECL58	HDL Laboratory	Dr. Vijaykumar T Mrs. Latha S	<i>[Signature]</i>	Dr. Supreeth H S G Mrs. Divyashree YV	<i>[Signature]</i>
HSMC: Humanity and Social Science						
9	18CIV59	Environmental Studies (1 hour/week)				

[Signature]
Co-ordinator
3/8/21

[Signature]
3.8.2021
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SUBJECT ALLOTMENT (UG) – ODD SEMESTER 2021 – 2022

VII Semester

Class Teacher: VII A: Dr. Darshan B D

VII B: Dr. M Lakshminarayana

Sl. No	Subject Code	Subject Title	Name of the Faculty				
			Section A	Sign	Section B	Sign	
Theory Subjects							
1	18EC71	Computer Networks	Dr. Kumutha D		Dr. Kumutha D		
2	18EC72	VLSI Design	Dr. K V MahendraPrashanth		Mrs. Chetana R		
Professional Elective – 2 (Common Slot for A&B)							
3	18EC731	Real Time Systems	Mrs. Nithya S				
	18EC732	Satellite Communication	Dr. Anitha P				
	18EC733	Digital Image Processing	Dr. Chandrappa D N				
Professional Elective – 3 (Common Slot for A&B)							
4	18EC741	IOT & Wireless Sensor Networks	Mr. Darshan B D		Dr. M Lakshminarayana		
	18EC744	Cryptography	Mrs. Uma S				
Open Elective – A (Common Slot for A&B)							
5	18CS751/752/754	Big Data/Python/Dot net	Dept. of CSE/ISE/EEE/Mech/Civil				
	18EE752	Electric Vehicle					
	18CV753	Env. Protection & Mgmt					
	18ME752/753/755	Auto. Engg./Industrial Safety/Energy & Env					
Laboratory							
6	18ECL76	Computer Networks Lab	Dr. Kumutha D & Ms. Geethanjali N		Mrs. Pushpalatha G Mrs. Chetana R (B1 & B2), Dr. Kumutha (B3)		
7	18ECL77	VLSI Laboratory	Mrs. Jyothi H & Mrs. Uma S		Mrs. Nithya S & Mrs. Supriya M		
8	18ECP78	Project Work Phase-I + Project work Seminar	Dr. Mahantesh K		Mr. Bhaskar B		

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2/8/21

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SUBJECT ALLOTMENT (PG) – ODD SEMESTER 2021 – 2022

M.Tech - I SEMESTER - VLSI DESIGN & EMBEDDED SYSTEMS

Class Teacher: Mrs. Nithya S

Sl. No	Subject Code	Subject Title	Name of the Faculty	Sign
1	20ELD11	Advanced Engineering Mathematics		
2	20EVE12	ASIC Design	Ms. Geethanjali N	
3	20EVE13	Advanced Embedded System	Dr. Chandrappa D N	
4	20EVE14	VLSI Testing	Mrs. Pushpalatha G	
5	20EVE15	Digital VLSI Design	Mrs. Divyashree YV	
6	20EVEL16	VLSI & ES Lab-1	Mrs. Jyothi H & Mrs. Nithya S	
7	20RMI17	Research Methodology and IPR	Mrs. Nithya S	

Note: Verify Scheme & Syllabus for Aca- 2021 – 2022 (M.Tech VLSI & ESD)

M.Tech - III SEMESTER - VLSI DESIGN & EMBEDDED SYSTEMS

Class Teacher: Mrs. Uma S

Sl. No	Subject Code	Subject Title	Name of the Faculty	Sign
1	20EVE31	CAD of Digital Systems	Mrs. Chetana R	
2	20XXX32X	Professional elective 3	Mrs. Uma S	
3	20XXX33X	Professional elective 4	Mrs. Jyothi H	
4	20EVE34	Project Work phase -1	Mrs. Uma S & Mrs. Anushree R	
5	20EVE35	Mini-Project	Mrs. Uma S & Mrs. Anushree R	
6	20EVEI36	Internship	Mrs. Uma S & Mrs. Anushree R	

Co-ordinator 3/8/21

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DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

SUBJECT ALLOTMENT (UG 2017 Scheme) – 7th SEMESTER 2021 – 2022

VII Semester 2017 Scheme

Class Teacher: Mrs. Anushree R

Sl. No	Subject Code	Subject Title	Name of the Faculty	Sign
1	17EC71	Microwave and Antennas	Dr. Komala M	
2	17EC72	Digital Image Processing	Mrs. Pushpalatha G	
3	17EC73	Power Electronics	Mr. Darshan B D	
4	17EC741	Multimedia Communication (PE3)	Mrs. Anushree R	
5	17EC755	Satellite Communication (PE4)	Mrs. Supriya M	
6	17ECL76	Advanced Communication Lab	Dr. Anitha P & Dr. D N Chandrappa	
7	17ECL77	VLSI Lab	Mrs. Supriya M & Mrs. Uma S	
8	17ECP78	Project Work Phase-I + Project work Seminar	Dr. Mahantesh K & Mr. Bhaskar B	

	Faculty Name	Sign
Basic Electronics (5 Sections)	Dr. Somashekar K	
	Mrs. Sowmya B J	
	Mrs. Anushree R	
	Mrs. Supriya M	
	Ms. Geethanajali N	
Open Elective (Neural Network – 18EC752)	Dr. Supreeth H S G	

Co-ordinator

3/8/21

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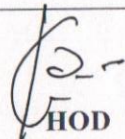
TEACHING-WORK LOAD STATEMENT

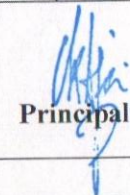
Department : Electronics & Communication Engineering
Period : ODD Semester - 2021-22

on: 02/08/2021

SL. No	Name of the Staff	Theory			Practical			Total workload (Hours)
		SEM	Subject Name	Total Hrs Per week	SEM	Subject Name	Total Hrs Per week	
1.	Dr. K V MahendraPrashanth	VII – A	VLSI Design	3			-	9
		Administrative Work						
2.	Dr. Rekha K R	III – A & B	Power Elec. & Inst.	6	III – B3	EDI Lab	4	10
3.	Dr. Chandrappa D N	VII – A & B I-M.Tech	Digital Image Processing Advanced Emb. Sys.	3 4	VII – 2017	Adv. Comm. Lab	3	10
4.	Dr. Vijaykumar T	III – A & B	Network Theory	10	V – A1 & A3	HDL Lab	8	20
5.	Dr. Somashekar K	V – A I – Sem	Info. Theory & Coding Basic Electronics	3 4	III – A	EDI Lab	12	19
6.	Dr. Komala M	III – A VII - 2017	Computer Org. & Arch. Microwave and Antennas	3 4	III – B	DSD Lab	12	19
7.	Dr. Mahantesh K	V – A & B	Electromagnetic Waves	6	V – B	DSP Lab	12	18
8.	Dr. Ravikumar A V	III – A & B	Digital System Design	6	III – A	DSD Lab	12	18
9.	Dr. Shilpa K Gowda	V – A & B	Principles of Comm. Sys	10	III – B	DSD Lab	12	22
10.	Dr. Supreeth H S G	V – B	Verilog HDL	3	V – B VII – A1 & A2	HDL Lab CN Lab	12 8	23
11.	Dr. Anitha P	VII – A & B	Satellite Communication	3	III – A VII - 2017	EDI Lab Adv. Comm. Lab	12 3	18
12.	Dr. M Lakshminarayana	V – B VII – B	Digital Signal Processing IOT & W/s Sensr. Net.	5 3	V – B	DSP Lab	12	20
13.	Dr. Sunitha Y N	V – A & B	Technological Inn. and Entre.	6	III – B	EDI Lab	12	18

14.	Dr. Kumutha D	Relieved on 24/12/2021						
15.	Dr. Manjunath M	VII - 2017 I - M. Tech	Digital Image Processing VLSI Testing	4 4	V - A2 VII - B VII - A3	HDL Lab VLSI Lab CN Lab	4 12 2	25
16.	Mrs. Chetana R	VII - B III - M. Tech	VLSI Design CAD of Digital Systems	3 4	VII - B	Computer Net. Lab	12	19
17.	Mr. Bhaskar B	III - A & B	Electronic Devices	6	III - B1 & B2	EDI Lab	8	14
18.	Mrs. Latha S	V - A	Verilog HDL	3	V - A III - B2	HDL Lab EDI Lab	12 4	19
19.	Mrs. Nithya S	VII - A & B I - M. Tech	Real Time Systems Design Research Met. and IPR	3 2	VII - B	VLSI Lab	12	17
20.	Mrs. Pushpalatha G	VII - A & B	Computer Networks	6	VII - B	Computer Net. Lab	12	18
21.	Mrs. Uma S	VII - A & B III - M. Tech	Cryptography Professional elective 3	3 4	VII - A	VLSI Lab	12	19
22.	Mrs. Divyashree YV	V - B I - M. Tech	Info. Theory & Coding Digital VLSI Design	3 4	V - B VII - A3	HDL Lab CN Lab	12 4	23
23.	Mrs. Jyothi H	III - B III-M. Tech	Computer Org. & Arch. Professional elective 4	3 4	VII - A	VLSI Lab	12	19
24.	Mrs. Sowmya B J	I - Sem	Basic Electronics	4	V - A VII - 2017	DSP Lab VLSI Lab	15	19
25.	Mrs. Anushree R	VII - 2017 I - Sem	Multimedia Communication Basic Electronics	4 4	III - A	EDI Lab	12	20
26.	Mrs. Supriya M	VII - 2017 I - Sem	Satellite Communication Basic Electronics	3 4	VII - B	VLSI Lab	12	19
27.	Ms. Geethanjali N	I - M. Tech I - Sem	ASIC Basic Electronics	4 4	VII - A	Computer Net. Lab	12	20
28.	Mr. Rahul Rai	V - A	Digital Signal Processing	5	V - A III - B1	DSP Lab EDI Lab	12 4	21
29.	Mr. Darshan B D	VII - A VII - 2017	IOT & W/s Sensr. Net. Power Electronics	3 4	III - A	DSD Lab	12	19


HOD


Principal